AMENDMENTS TO THE CLAIMS

1. (Currently Amended): A multi-domain liquid crystal display device, comprising:

first and second substrates;

a liquid crystal layer between the first and second substrates;

a plurality of data lines for applying a data signal on the first substrate;

a plurality of gate lines for applying a gate signal, the gate lines crossing the data lines to define a plurality of pixel regions, wherein each pixel region has a multi-domain structure which includes a dielectric structure or a slit;

a thin film transistor (TFT) near each crossing of the gate lines and the data lines;

a common electrode on the second substrate;

a pixel electrode connected to a drain electrode of the thin film transistor in each pixel region; and

an auxiliary electrode line electrically connected to at least one of the gate lines in each pixel region, the auxiliary electrode line and the multi-domain structure distorting an electric field applied between the common electrode and the pixel electrode to thereby form at least two domains in each pixel region during an operation of the multi-domain liquid crystal display,

wherein the auxiliary electrode line is formed between the pixel electrode and the data line at an outside of the pixel electrode in the pixel region and the auxiliary electrode is spaced apart from ~~not overlapped with~~ the data line.

2-23. (Cancelled)

24. (Currently Amended): The device according to claim [[3]] 1, wherein the common electrode includes an opening area.

25. (Previously Presented): The device according to claim 1, wherein the dielectric structure is on the second substrate.

26. (Previously Presented): The device according to claim 1, wherein the liquid crystal layer has a positive dielectric anisotropy.

27. (Previously Presented): The device according to claim 1, wherein the liquid crystal layer has a negative anisotropy.

28. (Previously Presented): The device according to claim 1, wherein the liquid crystal layer includes a chiral dopant.

29. (Previously Presented): The device according to claim 1, further comprising a phase-differential film on at least one of the first and second substrates.

30. (Previously Presented): The device according to claim 29, wherein the phase-differential film includes a negative uniaxial film.

31. (Previously Presented): The device according to claim 29, wherein the phase-differential film includes a negative biaxial film.

32. (Previously Presented): The device according to claim 1, wherein the auxiliary electrode line is formed in the same layer as the gate lines.